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[57] ABSTRACT

A package for mounting I/C chips onto a circuit board is provided. The chip has a surface array of input/output pads on one side which forms a footprint. A carrier is formed of an organic dielectric material having opposite surfaces. A first set of bonding pads is formed on one surface of the chip carrier and arranged to correspond with the chip footprint. A first set of solder ball connections connects the input/output pads on the chip to the first set of bonding pads on the chip carrier. A second set of bonding pads is formed on the other surface of the chip carrier forming a second set of bonding pads. Electrically conducting vias extend through the chip carrier connecting the first set of bonding pads to the second set of bonding pads. An organic circuit board having a coefficient of thermal expansion similar to the chip carrier having electrical connector sites is provided, the sites are arranged in a pattern corresponding to the pattern of the second bonding pads on the chip carrier. A second set of solder ball connections connect the pads of the second set of bonding pads on the chip carrier to the connection sites on the circuit board.

12 Claims, 3 Drawing Sheets

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[54] IC CHIP ATTACHMENT

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